IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants:	DEBASHIS BHATTACHARYA et al.
Serial No:	09/896,059
For:	METHOD FOR AUTOMATED DESIGN OF INTEGRATED CIRCUITS WITH TARGETED QUALITY OBJECTIVES USING DYNAMICALLY GENERATED BUILDING BLOCKS
Filed:	JUNE 29, 2001
Examiner:	THOMPSON, ANNETTE M.
Art Unit:	2825
Docket No.:	162.7107USU

9th Amdt a

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 2313-1450

AMENDMENT

Dear Sir:

In response to the Office Action dated March 28, 2003, please enter and consider the following amendments and remarks in the above-noted application.